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1	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801"
2	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$4
3	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$4 and insert\$4 same (buffer driver repeater inverter) same global adj3 (rout\$4 signal connect\$4 interconnect\$4)
4	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$4 and insert\$4 same (buffer driver repeater inverter) same global adj3 (rout\$4 signal net connect\$4 interconnect\$4)
5	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$4 and insert\$4 same (buffer driver repeater inverter) same global adj3 (rout\$4 signal net connect\$4 interconnect\$4) and (plac\$7 with (macro module block))
6	insert\$4 near3 (buffer driver repeater inverter) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$4 and insert\$4 same (buffer driver repeater inverter) same global adj3 (rout\$4 signal net connect\$4 interconnect\$4) and ((plac\$7 layout\$4) with (macro module block))
7	"20040257207" and electron\$4 same signal same instruction
8	insert\$4 same (buffer driver repeater inverter) same (number or (how adj many)) same global and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801"
9	(insert\$4 near5 (buffer driver repeater inverter)) same (number or (how adj many)) same global and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801"
10	((insert\$4 near5 (buffer driver repeater inverter)) same (number or (how adj many)) same global and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and ((plac\$7 layout\$4) with (macro module block))
11	((insert\$4 near5 (buffer driver repeater inverter)) same (number or (how adj many)) same global and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5
12	((insert\$4 near5 (buffer driver repeater inverter)) same (number) same global adj (rout\$4 interconnect\$4 net signal) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5
13	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same (number) same global adj (rout\$4 interconnect\$4 net signal) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5
14	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$4 net signal) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5
15	"6453446".pn. and (block maco module insert\$5 add\$5 plac\$4 layout\$6 global)
16	"6453446".pn. and (block maco module insert\$5 add\$5 plac\$4 layout\$6 global) and (insert\$5 add\$5 plac\$7) near5 (buffer driver repeater inverter)
17	"6453446".pn. and (insert\$5 add\$5 plac\$7) near5 (buffer driver repeater inverter) same timing
18	"6453446".pn. and (insert\$5 add\$5 plac\$7) near5 (buffer driver repeater inverter) same timing same (position\$4 locat\$6)

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19	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$4 net signal) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5 and ((plac\$7 layout\$4) same (macro module block)) and physical near5 hierarch\$6
20	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$4 net signal) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5 and ((plac\$7 layout\$4) same (macro module block))
21	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$6 net signal) same (timing constraint) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5 and ((plac\$7 layout\$4) same (macro module block))
22	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$6 net signal) same (timing constraint) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5 and (partition\$5) with (macro module block)
23	((insert\$4 add\$6 plac\$6) near5 (buffer driver repeater inverter)) same global adj (rout\$4 interconnect\$6 net signal) same (timing constraint) and (position\$4 location\$5) near3 (buffer driver repeater inverter) and @ad<"20010801" and global adj rout\$5 and (floor\$9) with (macro module block)